

Docket Number: 081498-0279404
Client Reference: GNC16CON1



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of

FRAMPTON E ELLIS

Group Art Unit: 2153

#8

Application No.: 09/884,041

Examiner:

Filed: June 20, 2001

Confirmation No.: 3126

For: GLOBAL NETWORK COMPUTERS

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INFORMATION DISCLOSURE STATEMENT

AUG 05 2004

Commissioner for Patents
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Sir:

Pursuant to 37 CFR 1.56, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom. Applicants respectfully request the Examiner return an initialed copy of the enclosed Form PTO-1449 to Applicants with the next Office communication to indicate that the references has been considered, per MPEP § 609.

This Information Disclosure Statement is being filed before the mailing date of the first Office Action on the merits in the present application. No certification or fee is required.

These references were cited in three Office Actions in three co-related U.S. applications.

Respectfully Submitted,

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PTO/SB/08b (08-03)

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Substitute for form 1449B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Complete if Known			
		Application Number	09/884,041		
		Filing Date	06/20/2001		
		First Named Inventor	FRAMPTON E ELLIS		
		Art Unit	2153		
		Examiner Name			
Sheet	1	of	1	Attorney Docket Number	081498-0279404

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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	A	NONE, "PC vision: Intel unveils plans to bring PCs to vehicles", EDGE: Work Group Computing Report, EDGE Publishing, p. 1-2, (October 28, 1996).	
	B	LI, YAO, "Free-space Optical Bus-based WDMA Interconnects for Parallel Computation," LEOS '92 Conference Proceedings, Lasers and Electron-Optics Society Annual Meeting, p. 588-589, (November 16-19, 1992).	
	C	DICKINSON ET AL., "An Integrated Free Space Optical Bus," 1989 IEEE International Conference on Computer Design" VLSI In Computers and Processors, p. 62-65, (October 2-4, 1989).	
	D	NATARAJAN ET AL, "Bi-Directional Optical Backplane Bus for General Purpose Multi-Processor," Journal of Lightwave Technology, Vol. 13 (No. 6), p. 1031-1040, (June 6, 1995).	
	E	ZHAO ET AL, "General Purpose Bidirectional Optical Backplane: High Performance Bus for Multiprocessor Systems," Massively Parallel Processing Using Optical Interconnections, 2nd International Conference, p. 188-195, (October 23-24, 1995).	
	F	WU ET AL, "Microprocessor Control Signal Transmission through Optical Fiber," Conference Record of 1992 IEEE Industry Applications Society Annual Meeting, p. 1747-1750, (October 4-9, 1992).	

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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